

## SYSOLUTION

# **LCD Controller L20B-D**

## **Specification Product**

Version: Ver.1.1

#### Statement

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## **Update Record**

No.	Version	Updates	Revision Date
1	Ver.1.0	Initial Release	2024.07.12
2	Ver.1.1	Add a size diagram	2024.07.30

The document is subject to change without prior notice.

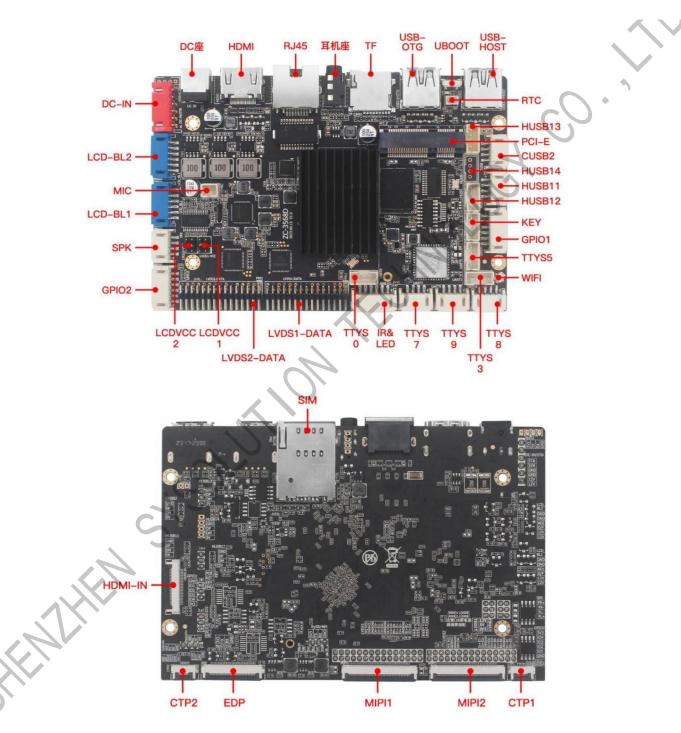
### **Product Introduction**

The L30 is powered by a high performance, low power consumption quad-core processor from Rexchip Microelectronics, RK3568 (Cortex-A55), equipped with Android 11, with an up to 2.0GHz main frequency. Embedded GPU (ARM G52 2EE) supports OpenGL ES 1.1/2.0/3.2, OpenCL 2.0, Vulkan 1.1. It integrates HDMI/LVDS/EDP/MIPI display interfaces, Ethernet/WIFI/BT/4G (4G module is required) expansion interfaces in one, which greatly simplifies the whole machine design. Support most of the current popular video and picture format decoding, high-performance VPU support for 4K 60fps H.265/H.264/VP9 video decoding, 1080P 60fps H.265/H.264 video encoding, 8M ISP and HDR. It can drive various TFT LCD displays. And with stronger stability, it is widely used in AI servers, face payment devices, security, medical, transport, finance, industrial control, intelligent education, smart retail and other AI intelligent fields.

#### **Functions:**

- High integration: Integrated dual LVDS or dual MIPI, EDP, HDMI display interface in one, the maximum support for dual-screen heterodyne, simplify the design of the whole machine, can be inserted into the TF card to modify the screen reference.
- 2. High stability: In hardware, software, increase our own unique development of technology to ensure the stability of the product, can make the final product to 7 \* 24 hours unattended.
- 3. Rich expansion interfaces: 6 USB interface (default 2 pins can be changed to 4-way pin interface, 2 standard USB3.0), 6 scalable serial port (default 3 can be changed to 6-way), GPIO/ADC interface, to meet the market requirements of a variety of peripherals.
- 4. High-definition: Maximum support 3840×2160 decoding and various LVDS/EDP interface LCD display.
- 5. Complete functions: Support dual-screen heterodyne display, horizontal and vertical screen playback, video split screen, scrolling subtitles, timer switch, USB data import and other functions;
- 6. Convenient management: Humanized playlist background management software, which is convenient for advertising playback management and control. Play log, easy to understand the playback situation.

## **Product Appearance**



headphone jack

## **Product Parameters**

Main hardwai	e parameters
CPU	Quad-core 64-bit Cortex-A55, up to 2.0 Ghz
GPU	ARM G52 2EE support OpenGL ES 1.1/2.0/3.2、OpenCL 2.0, Vulkan 1.1
NPU	Arithmetic power up to 1 TOPS
Memory	2G/ 4G(Optional)
Built-in	EMMC 16G (Be higher and customizable if required)
Memory	Livilvic 100 (be higher and customizable in required)
Operating	Android 11
System	Android 11
Play Mode	Support multiple playback modes such as loop, timing, insertion and so on
Video	
Format	Support wmv, avi, flv, rm, rmvb, mpeg, ts, mp4, etc.
Support	
Image	
Format	Support BMP、JPEG、PNG、GIF
Support	
Power	1 internal 2.54MM 6P power input interface and 1 OD 5.5 core 2.0 DC header
Supply	interface
Interface	interruce

LVDS Output	2 single/dual 30 PIN Dupont interfaces to directly drive 50/60Hz multi-resolution LCDs					
MIPI Output	2 x 4 LIN MIPI screens (40P 0.5MM pitch FPC interface), can directly drive 2 x MIPI LCD screens					
EDP Output	1 x 2 LIN EDP screen (30P 0.5MM pitch FPC connector), up to 1920*1080 output support					
HDMI Output	1 A-type interface, support for 1080P, 2K, 4K outputs					
TF card	Support 16G/32G/64G/128G (Theoretically, as long as SD3.0,MMC ver4.51 protocol, larger capacity is also supported)					
CTP interface	2-way I2C touch screen interface, can drive two I2C touch screen use at the same time					
Remote  Control and  Indicator  Lights	Support infrared remote control key control, support equipment working status light indication					
Serial/Expans ion Interface	Six channels TTL, eight channels GPIO					
Audio and video outputs	1 x 4-wire headphone jack (U.S. standard), 1 x dual-channel speaker output jack (Support left and right channel output, built-in dual 8R/5W power amplifier)					
Audio Inputs	1 microphone interface (2 PIN pins)					

Gravitational	Support, no posting by default		
Inductance	cappert, ne pesting by delacit		
RTC Real	Support timer on/off		
Time Clock	Support timer on/on		
USB	2 LICE 2.0. 5 intermed LICE as already		
Interface	2 USB 3.0, 5 internal USB sockets		
System			
Upgrade	Support upgrade by local USB , wireless and PC		
	1. Support 10/100M/1000M adaptive Ethernet		
Network	2. Built-in WiFi, Bluetooth, WIFI support hotspot sharing		
Support	3. Expandable 4G Internet access (USB 4G module required)		
Size	127.5mm*84.50mm/±0.5mm, Plate thickness 1.6mm±10%		
Working	Temperature: -20°C to 70°C, recommended 5°C to 35°C, humidity: 10% to 9		
Environment	no condensation		
	Temperature: -20°C to 70°C, recommended 5°C to 35°C, humidity: 10% to 9 no condensation		
M/N.			
CAL			

## **Interface Parameters/Definitions**

#### DC-IN (Power Horizontal Socket 2.54MM Red)

Serial	Definiti	Attribute	Descriptio	on
number	on			•
1	DC12V	Power	12V power input	0.
·	-IN	Input	. 21 ponte impat	C
2	DC12V	Power	12W power input	20
2	-IN	Input	12V power input	
3	GND	Power	Power Ground	
3	GND	Ground	rower diodria	
4	CNID	Power		DEBUG DEBUG
4	GND	Ground	Power Ground	::# <u> </u>
	rvc to	Signal	CTD nouver in suit	
5	5VSTB	Input	STB power input	
	ОТР	Signal	CTD signal autout	
6	STB	Output	STB signal output	

- 1. Connect to this socket when using the built-in power input;
- 2. The STB function requires the support of the external power board to be used;
- 3. The power supply voltage is 12V input, the use range is acceptable between 9V-14V, don't use the power adapter which exceeds this range;

4. 2.54 Socket single PIN rated current 2.5A, 2PIN seat maximum 5A, please do not exceed this current.

LCD-BL1 (LVDS Screen Backlight Horizontal Socket 2.00MM Blue)

Serial numb er	Definition	Attribute	Description	60.
1	BL-12V_IN	Power Output	12V backlight power output, 12V power supply directly	3
2	BL-12V_IN	Power Output	backlight board, the size of the current depends on the current of the adapter	
3	ON / OFF	Control Output	Backlight panel switch signal, active high, software configurable	SPK LCD BLI LCD BLZ
4	ADJ	Control Output	PWM control of LVDS screen brightness	
5	GND	Power Ground	Power Ground	
6	GND	Power Ground	Power Ground	

- 1. Pay attention to the order of the feet, can not be connected to the reverse;
- 2. For models that do not need to use the ADJ function, the ADJ can be directly suspended

or connected to the ON/OFF, so as to avoid the problem of screen darkness, the ADJ is connected to the high or low, need to check the screen specifications to determine.

LCD-BL2 (LVDS Screen Backlight Horizontal Socket 2.00MM Blue)

Serial numb er	Definition	Attribute	Description	60.
1	BL-12V_IN	Power Output	12V backlight power output, 12V power supply directly to the	57
2	BL-12V_IN	Power Output	backlight board, the size of the current depends on the current of the adapter	
3	ON / OFF	Control	Backlight panel switch signal, active high	
4	ADJ	Control Output	PWM control of LVDS screen brightness	
5	GND	Power Ground	Power Ground	
6	GND	Power	Power Ground	

- 1. Pay attention to the order of the feet, can not be connected to the reverse;
- 2. For models that do not need to use the ADJ function, the ADJ can be directly suspended or connected to the ON/OFF, so as to avoid the problem of screen darkness, the ADJ is

connected to the high or low, need to check the screen specifications to determine.

#### LCDVCC1 (LVDS-LOGIC power input jumper selector vertical pin 2.00MM)

Serial				
numb	Definition	Attribute	Description	
er				1
1	BL-3.3V IN	Power	3.3V power input, jump cap	0.
	_	Input	connection	2
2	BL-VCC-O	Backlight	LVDS_LOGIC power output	
_	UT	Output	EVDS_LOGIC power output	
3	BL-5.0V_IN	Power	5.0V power input, jump cap	
	DE 3.0V_IIV	Input	connection	
4	BL-VCC-O	Backlight	LVDS_LOGIC power output	LVDS1-VCC
	UT	Output	LVD3_LOGIC power output	
		Power	12V power input, jump cap	
5	BL-12V_IN	Input	connection	
	BL-VCC-O	Backlight	IVDS LOCIC requires quitro it	
6	UT	Output	LVDS_LOGIC power output	

- 1. After connecting the LCD screen, be sure to pay attention to how much logic voltage is needed for the display, and jump the jump cap to the corresponding voltage selection above the PIN foot, otherwise it is easy to burn out the display circuit. (On the display voltage, please consult the corresponding screen specification) It is very important;
- 2. This jump cap voltage selection and LCD1 screen with the use.

#### LCDVCC2 (Power Input Jump-Select Vertical Pin 2.00MM)

Serial numb er	Definition	Attribute	Description	
1	BL-3.3V_IN	Power Input	3.3V power input, jump cap connection	cO.,
	BL-VCC-O	Backlight		40
2	UT	Output	LVDS_LOGIC power output	20,
3	BL-5.0V IN	Power	5.0V power input, jump cap	
	BE 3.6V_IIV	Input	connection	
4	BL-VCC-O	Backlight	LVDS_LOGIC power output	T SE LVO
4	UT	Output	LVD3_LOGIC power output	
5	BL-12V IN	Power	12V power input, jump cap	
	2F 1F1-111	Input	connection	
6	BL-VCC-O	Backlight	LVDS_LOGIC power output	
	UT	Output	LVD3_LOGIC power output	

- 1. After connecting the LCD screen, be sure to pay attention to how much logic voltage is needed for the display, and jump the jump cap to the corresponding voltage selection above the PIN foot, otherwise it is easy to burn out the display circuit. (On the display voltage, please consult the corresponding screen specification) It is very important;
- 2. This jump cap voltage selection and LCD2 screen with the use.

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#### **UBOOT Button**

Serial			
numb	Definition	Attribute	Description
er			
1	GND	Ground	Ground
2	LIDOOT	l.a.a.v.t	UBOOT function status
2	UBOOT	Input	selection

1. When this button is held down and then switched on, the device will enter the firmware UBOOT burning mode.

#### **WIFI Antenna Holder**

Serial				
numb	Definition	Attribute	Description	
er				
1	GND	Ground	Ground	
2	RF C	Signal Input	WIFI, BT signal input	

- 1. Note that the WIFI antenna holder is IPEX-2 generation holder, please match the external antenna bar with the 2 generation female holder.
- 2. Regarding the connection between the antenna holder and the PCB fixed pad is small.

  When disassembling, please pay attention to the light buckle to take out, to avoid directly pulling out and lead to the antenna holder and the PCB separation can not be repaired.

#### MIC (Microphone Standing Socket 2.00MM)

Serial				
numb	Definition	Attribute	描述 Description	
er				
1	MIC	Mic input	丰丰四工+T Mi ava ala a a Dacitiva	
1	MIC+	+	麦克风正极 Microphone Positive	W.C.
2	MIC-	Mic input -	Microphone Negative	C

 The MIC+ of the microphone connector and the headphone connector are the same way, and can only be used in one of the two ways.

#### RTC (Battery Vertical Pin 1.25MM)

Serial				
numb	Definition	Attribute	Description	
er				
1	GND	Ground	Ground	
2	RT+	RTC Clock Power	Power supply output, hold	
		Supply	system time	

1. External 2032 coin cell battery with extension cable for RTC.

#### **Headphone Holder (Quad 3.5MM Interface)**

Serial				
numb	Definition	Attribute	Description	
er				
1	PL	L-OUT	left channel output	
2	PR	R-OUT	right channel output	
3	SNS	GND	Ground	3 3 3 3
4	MIC+		microphone input	A Private Control

1. The MIC+ of the headphone jack and the microphone 2PIN connector are the same way, only one can be used in either way.

#### SPK (Speakers Horizontal Sockets 2.00MM)

Serial			.0			
numb	Definition	Attribute	Attribute			
er			) '			
1	L+	L output	Speaker	amplifier	output	
'		positive	positive			
2		L output	Speaker	amplifier	output	
		negative	negative			11 11
3	R-	R output	Speaker	amplifier	output	IVIOZAVCE SPK
	K	negative	negative			
4	R+	R output	Speaker	amplifier	output	
4	<b>       </b>	positive	positive			

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- This is a double speaker connection, when using a single speaker is PIN1 and PIN2 group,
   PIN3 and PIN4 group, can not be mistaken;
- 2. The use of the speaker, need to connect the speaker before switching on, do not allow the use of power unplugging. Default use of 8-ohm speakers;
- 3. Speaker interface power output characteristics (limit conditions: TA = 25 °C, DC = 12.0V)
- 4. Amplifier chip default 2 \* 8 ohm / 5W, pay attention to the use of the speaker matching interval. It is recommended that the speaker rated power can be achieved in more than 3W.

  Chip can support up to 2 \* 8 ohm / 10W (need to change the hardware parameters)

#### **KEY (External Vertical Socket 2.00MM)**

Serial			, CKIII	
numb	Definition	Attribute	Description	
er			A	
1	POE	POWEN	System Boot Button	
2	RST	RESET	Reset Signal Interface	ISOH I WILL I I I I I I I I I I I I I I I I
3	KEY	15)	KEY expansion interface (up to	
3	NEY C	Input	7 keys)	C P A
4	GND	Ground	Ground	

1. The configuration of the keys can be adjusted, subject to the actual communication requirements. The specific use of please refer to the "Zhuoche card physical key production instructions.

#### CTP1 (6PIN 0.5MM FPC Socket)

Serial numbe r	Definition	Attribute	Description	
1	vcc	3.3V	power supply	
2	GND	Ground	Ground	$C_O$ .
3	SCL	I2C- SCL	I2C clock, compatible with GPIO, CAN0-TX	54
4	SDA	I2C-SDA	I2C data, compatible with GPIO, CAN0-RX	(P) 111111 图
5	INT	Interrupt	CTP interrupt, compatible with	
6	RST	Reset	CTP reset, compatible with	

1. Default is CTP interface, when want to change to GPIO or CAN port, you need to reconfigure the software.

#### CTP2 (6PIN 0.5MM FPC Socket)

Serial numbe r	Definition	Attribute	Description	
1	VCC	3.3V	power supply	至例の
2	GND	Ground	Ground	6

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3	SCL	I2C- SCL	I2C clock, compatible with	
3	SCL	12C- 3CL	GPIO, CAN1-TX	
4	SDA	I2C-SDA	I2C data, compatible with	
4	SDA	12C-3DA	GPIO, CAN1-RX	
5	INT	Interrupt	CTP interrupt, compatible with	
3	IIVI	interrupt	GPIO	$\mathcal{O}_{\mathcal{O}}$ .
6	RST	Reset	CTP reset, compatible with	C
O	אסו	neset	GPIO	20

<sup>1.</sup> Default is CTP interface, when want to change to GPIO or CAN port, you need to reconfigure the software.

#### IR&LED (Remote Control Indicator Horizontal Socket 2.00MM)

Serial	Definition	Attribute	Description	
number	Delimition	Attribute	Description	
	4	5	Positive pole of RED lamp,	
1	LED-RED	Output	system operation status	
			indicator	
2	GND	Power	Power Ground	
		ground		T.
			Positive pole of BLUE lamp /	
3	LED-BLUE	Output	System shutdown status	
			indicator	

4	IRVCC-3V3	Power Input	Remote power output	
5	GND	Power Ground	Power Ground	
6	IR-IN	Signal Input	IR signal input	60.

- Indicator lamps use common cathode LED lamps by default. If a common anode lamp is
  used, the common pin of the LED lamp can be connected to the 3rd PIN as the power
  input when making an external extension cable. Note that the state of the light will change
  after this connection, and software configuration update is required;
- 2. Remote control supports hard switching function. Remote power on button needs software configuration, or remote code value learning to match before use;
- 3. About the remote control that needs to be reconfigured to make sure the buttons are available. If use a remote control that is not certified by our company, need to communicate with the business to confirm.

#### **GPIO1 (Horizontal Socket 2.00MM)**

Serial numbe	Definition	Attribute	Description	
r				
1	GND	Ground	Ground	
2	GPIO1	Input/Out	Default GPIO port, compatible	0.
	drioi	put	with RST for CTP	1
3	GPIO2	Input/Out	Default GPIO port, compatible	
3	GF102	put	with INT for CTP.	
4	GPIO3	Input/Out	Default GPIO port, compatible	
4	drios	put	with SCL,CAN1-TX for CTP.	
5	GPIO4	Input/Out	Default GPIO port, compatible	3
J	GF104	put	with SDA, CAN1-RX for CTP.	
6	VCC-3.3V	Power Output	VCC-3.3V	

- 1. The port is GPIO port by default. When used as CTP or CAN port, it needs to be configured separately by software;
- 2. I/O port voltage is 3.3V, pay attention to the level matching.

#### **GPIO2** (Horizontal Socket 2.00MM)

Serial	Definiti	Attribute	Description	
number	on			
1	GND	Ground	Ground	E E

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2	GPIO1	Input/Out	Default GPIO port, compatible	
	GFIOT	put	with RST for CTP	
3	GPIO2	Input/Out	Default GPIO port, compatible	
3	GPIO2	put	with INT for CTP	
4	CDIO2	Input/Out	Default GPIO port, compatible	
4	GPIO3	put	with SCL,CAN0-TX for CTP	$\mathcal{O}_{\mathcal{O}}$ .
5	CDIO4	Input/Out	Default GPIO port, compatible	A
5	GPIO4	put	with SDA, CAN0-RX for CTP	00
	VCC-3.3	Power	VCC 2.2V	
6	V	Output	VCC-3.3V	

- 1. The port is GPIO port by default. When used as CTP or CAN port, it needs to be configured separately by software;
- 2. I/O port voltage is 3.3V, pay attention to the level matching.

#### TTYS0 (UART/RS485 Vertical Socket 2.00MM)

Serial	Definition	120		
numbe	C	Attribute	Description	
r				
	VCC-3.3V	Power	VCC-3.3V	
	VCC-3.3V	Output	VCC-3.3V	
		Data		DINI
2	UART_TX0	Transmissi	UART_TX, compatible with	
		on	GPIO,PWM	

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2	3 UART_RX0	Data	UART_RX, compatible with	
3		Reception	GPIO,PWM	
4	GND	Ground	Ground	

- 1. Default is 1 TTL form of output, can add chip to change to RS485, port number is ttyS0;
- 2. 3 V 3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### TTYS8 (UART/RS485 Horizontal Socket 2.00MM)

Serial numbe	Definition	Attribute	Description
1	VCC-3.3V	Power Output	VCC-3.3V
2	UART_TX8	Data Transmissi on	UART_TX, compatible with GPIO,PWM
3	UART_RX8	Data Reception	UART_RX, compatible with GPIO,PWM
4	GND	Ground	Ground

- 1. Default is 1 TTL form of output, can add chip to change to RS485, port number is ttyS8;
- 2. 3V3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### TTYS7 (UART/RS232 Horizontal Socket 2.00MM)

Serial numbe r	Definition	Attribute	Description	
1	VCC-3.3V	Power Output	VCC-3.3V	60.
2	UART_TX7	Data Transmissi on	UART_TX, compatible with GPIO,PWM	UARTY UARTS
3	UART_RX7	Data Reception	UART_RX, compatible with GPIO,PWM	
4	GND	Ground	Ground	

- 1. Default is 1 TTL form of output, can add chip to change to RS232, port number is ttyS7;
- 2. 3V3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### TTYS9 (UART/RS232 Horizontal Socket 2.00MM)

Serial numbe r	Definition	Attribute	Description	
1	VCC-3.3V	Power	VCC-3.3V	UARTS DID CUARTS
•	VCC 3.3V	Output	VCC 3.3 V	

		Data	UART TX, compatible with
2	UART_TX9	Transmissi	GPIO,PWM
		on	G. 167 T
3	LIADT DVO	Data	UART_RX, compatible with
3	UART_RX9	Reception	GPIO,PWM
4	GND	Ground	Ground

- 1. Default is 1 TTL form of output, can add chip to change to RS232, port number is ttyS9;
- 2. 3V3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### TTYS3 (UART/RS232 Vertical Socket 2.00MM)

Serial			
numbe	Definition	Attribute	Description
r			
1	VCC-3.3V	Power Output	VCC-3.3V
2	UART_TX3	Data Transmissi on	UART_TX, compatible with GPIO,PWM
3	UART_RX3	Data Reception	UART_RX, compatible with GPIO,PWM
4	GND	Ground	Ground

- 1. Default is 1 TTL form of output, can add chip to change to RS232, port number is ttyS3;
- 2. 3V3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### TTYS5 (UART/RS232 Vertical Socket 2.00MM)

Serial numbe	Definition	Attribute	Description
1	VCC-3.3V	Power Output	VCC-3.3V
2	UART_TX5	Data Transmissi on	UART_TX, compatible with GPIO,PWM
3	UART_RX5	Data Reception	UART_RX, compatible with GPIO,PWM
4	GND	Ground	Ground

- 1. Default is 1 TTL form of output, can add chip to change to RS232, port number is ttyS5;
- 2. 3V3 output voltage is 3.3V; output current is 500MA;
- 3. When to use as GPIO port, need software to configure separately.

#### CUSB2 (Horizontal Socket 2.00MM)

Serial			
numbe	Definition	Attribute	Description
r			

1	GND	Ground	Ground	
2	DP	Data	Data positive, connect to	
2	DF	Positive	USB_DP pin of external device	
3	DM	Data	Data negative, connect to	
3	DIVI	Negativity	USB_DM pin of external device	CORS STORY
4	VCC-5V	Power	Power cable	$C_O$ .
4	VCC-5V	Output	Power Cable	C

- 1. This USB port is the main control directly out of the USB port;
- 2. For large data transmission interaction, priority to use this interface.

#### **HUSB11 (Horizontal Socket 2.00MM)**

Serial			
numbe	Definition	Attribute	Description
r			
1	GND	Ground	Ground
2	DP C	Data	Data positive, connect to
2	DP	Positive	USB_DP pin of external device
3	DM	Data	Data negative, connect to
	DIVI	Negativity	USB_DM pin of external device
4	VCC-5V	Power	Power cable
4	VCC-3V	Output	rower capie

1. This port is the USB port coming out of the HUB.

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#### **HUSB12 (Vertical Socket 2.00MM)**

Serial numbe	Definition	Attribute	Description	
1	GND	Ground	Ground	
2	DP	Data	Data positive, connect to	$\mathcal{O}$ .
2	DP	Positive	USB_DP pin of external device	P. J. GSON
3	DM	Data	Data negative, connect to	
3	DIVI	Negativity	USB_DM pin of external device	SSOH USOH THE TOTAL
4	VCC-5V	Power	Power cable	
4	VCC-3V	Output	rower cable	

1. This port is the USB port coming out of the HUB.

#### **HUSB13 (Vertical Socket 2.00MM)**

Serial		120	
numbe	Definition	Attribute	Description
r			
1	GND	Ground	Ground wire
	DD	Data	Data positive, connect to
2	DP	Positive	USB_DP pin of external device
3	DM	Data	Data negative, connect to
3	DM	Negativity	USB_DM pin of external device

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1	VCC-5V	Power	Power cable	
4	VCC-5V	Output	rower cable	

1. This port is the USB port coming out of the HUB.

#### **HUSB14 (Vertical Socket 2.00MM)**

Serial numbe	Definition	Attribute	Description	60.
r				2
1	GND	Ground	Ground wire	90
2	DP	Data	Data positive, connect to	
2		Positive	USB_DP pin of external device	. J. J. asno
3	DM	Data	Data negative, connect to	0000
3	DIVI	Negativity	USB_DM pin of external device	SSUH S
4	VCC-5V	Power		
		Output	Power cable	

- 1. This port is the USB port coming out of the HUB.
- 2. This seat, interoperable with PCI\_E socket, is not affixed by default.

#### EDP (30PIN 0.5MM Flip-top)

Serial numbe	Definition	Attribute	Description	
1	NC			
2	GND	Ground	Ground	

3	EDP-TX1N	Data channel 1 Negative	Data bit	ACTE (CITE OF STATE O
4	EDP-TX1P	Data channel 1 Positive	Data bit	
5	GND	Ground	Ground	The red box marks the
6	EDP-TX0N	Data channel 0 Negative	Data bit	first leg
7	EDP-TX0P	Data channel 0 Positive	Data bit	
8	GND	Ground	Ground	
9	EDP-TXCLK	Clock position Positive	Clock position	
10	EDP-TXCLK	Clock position Negative	Clock position	
11	GND	Ground	Ground	
12	VCC3V3	Power	Power supply 3.3V	

				1
		Supply		
13	VCC3V3  Supply		Power supply 3.3V	
14	NC			
15	GND	Ground	Ground	
16	GND	Ground	Ground	
17	HPD	HPD	HPD	2
18	GND	Ground	Ground	3
19	GND	Ground	Ground	
20	GND	Ground	Ground	
21	GND	Ground	Ground	
22	EN	EN	Backlight enable	
23	ADJ	ADJ	Backlight adjustment	
24	NC			
25	NC	(2)		
26	VDD	Power Supply	12V power supply	
27	VDD	Power	12V power supply	
		Supply		
28	VDD	Power	12V power supply	
	<b>VDD</b>	Supply	127 power suppry	
29	VDD	Power	12V power supply	

www.sysolution.net <sup>29</sup>

		Supply	
30	NC		

1. There is a 1-pin start bit on the main board.

#### LVDS-DATA1 (Dual Row LVDS Screen Horizontal Socket 2.00MM)

		1		
Serial numbe	Definition	Attribute	Description	40.,
1			LCD power supply,	00
2	LCDVCC-I	Power	+3.3V/+5V/+12V selectable	
3	N	Output	via "LVDS power jump cap input socket".	
4		Power	CP .	
5	GND	Ground	Power Ground Wire	STATA STATA
6		Wire		
7	RXO0-	Output	Pixel0 Negative Data (Odd)	<b>=</b> ⇒0
8	RXO0+	Output	Pixel0 Positive Data (Odd)	
9	RXO1-	Output	Pixel1 Negative Data (Odd)	
10	RXO1+	Output	Pixel1 Positive Data (Odd)	The red box marks the
11	RXO2-	Output	Pixel2 Negative Data (Odd)	first leg
12	RXO2+	Output	Pixel2 Positive Data (Odd)	
13	GND	Ground Wire	Ground Wire	

14	GND	Ground Wire	Ground Wire	
15	RXOC-	Output	Negative Sampling Clock (Odd)	
16	RXOC+	Output	Positive Sampling Clock (Odd)	
17	RXO3-	Output	Pixel3 Negative Data (Odd)	
18	RXO3+	Output	Pixel3 Positive Data (Odd)	
19	RXE0-	Output	Pixel0 Negative Data (Even)	
20	RXE0+	Output	Pixel0 Positive Data (Even)	
21	RXE1-	Output	Pixel1 Negative Data (Even)	
22	RXE1+	Output	Pixel1 Positive Data (Even)	
23	RXE2-	Output	Pixel2 Negative Data (Even)	
24	RXE2+	Output	Pixel2 Positive Data (Even)	
25	GND	Ground Wire	Ground Wire	
26	GND	Ground Wire	Ground Wire	
27	RXEC-	Output	Negative Sampling Clock (Even)	
28	RXEC+	Output	Positive Sampling Clock (Even)	
29	RXE3-	Output	Pixel3 Negative Data (Even)	
30	RXE3+	Output	Pixel3 Positive Data (Even)	
	•			

1. There is a 1-pin start bit on the main board.

#### LVDS-DATA2 (Dual Row LVDS Screen Horizontal Socket 2.00MM)

Serial					
numbe	Definition	Attribute	Description		
r					
1			LCD power supply,	co.	
2	LCDVCC-I	Power	+3.3V/+5V/+12V selectable	CH	
2	N	Output	via "LVDS power jump cap	00	
3			input socket".		
4		Power	, CKI		
5	GND Ground		Power Ground Wire	S GPIO	
6		Wire	(A)	- 30 Kg	
7	RXO0-	Output	Pixel0 Negative Data (Odd)	TATA TATA	
8	RXO0+	Output	Pixel0 Positive Data (Odd)		
9	RXO1-	Output	Pixel1 Negative Data (Odd)		
10	RXO1+	Output	Pixel1 Positive Data (Odd)		
11	RXO2-	Output	Pixel2 Negative Data (Odd)	The red box marks the	
12	RXO2+	Output	Pixel2 Positive Data (Odd)	first leg	
13	CND	Ground	Cround Wire		
13	GND	Wire	Ground Wire		
14	CND	Ground	Ground Wire		
14	GND	Wire	Ground wire		

15	RXOC-	Output	Negative Sampling Clock (Odd)	
16	RXOC+	Output	Positive Sampling Clock (Odd)	
17	RXO3-	Output	Pixel3 Negative Data (Odd)	
18	RXO3+	Output	Pixel3 Positive Data (Odd)	
19	RXE0-	Output	Pixel0 Negative Data (Even)	C
20	RXE0+	Output	Pixel0 Positive Data (Even)	6
21	RXE1-	Output	Pixel1 Negative Data (Even)	20
22	RXE1+	Output	Pixel1 Positive Data (Even)	
23	RXE2-	Output	Pixel2 Negative Data (Even)	
24	RXE2+	Output	Pixel2 Positive Data (Even)	
25	GND	Ground Wire	Ground Wire	
26	GND	Ground Wire	Ground Wire	
27	RXEC-	Output	Negative Sampling Clock (Even)	
28	RXEC+	Output	Positive Sampling Clock (Even)	
29	RXE3-	Output	Pixel3 Negative Data (Even)	
30	RXE3+	Output	Pixel3 Positive Data (Even)	

1. There is a 1-pin start bit on the main board.

## MIPI1 (40PIN 0.5MM Flip-top)

Serial numbe	Definition	Attribute	Description	
1	VDD1V8	1.8V	VDD1.8V power supply	
2	VDD3V3	3.3V	VDD3.3V power supply	$C_O$ .
3	VDD3V3	3.3V	VDD3.3V power supply	2
4	NC			
5	RESET	Screen Reset	Screen reset, low effective	
6	NC			
7	GND	Ground	Ground	
8	MIPI-DO-	Data channel 0 Negative	Data bit	
9	MIPI-DO+	Data channel 0 Positive	Data bit	The red box marks the
10	GND	Ground	Ground	ill'st leg
11	MIPI-D1-	Data channel 1 Negative	Data bit	

12	MIPI-D1+	Data channel 1 Positive	Data bit	
13	GND	Ground	Ground	
14	MIPI-CLK-	Clock	Clock position	
	IVIII I CEIX	Negative	Clock position	0
15	MIPI-CLK+	Clock	Clock position	(2)
		Positive		
16	GND	Ground	Ground	
		Data	'CCI'	
17	MIPI-D2-	channel 2	Data bit	
		Negative	,019	
		Data		
18	MIPI-D2+	channel 2	Data bit	
		Positive		
19	GND	Ground	Ground	
		Data		
20	MIPI-D3-	channel 3	Data bit	
		Negative		
		Data		
21	MIPI-D3+	channel 3	Data bit	
		Positive		

22	GND	Ground	Ground	
23	NC			
24	NC			
25	GND	Ground	Ground	
26	NC			
27	NC			0.
28	NC			(1)
29	NC			
30	GND	Ground	Ground	
31	LED-	Backlight	Negative feedback constant	
31	LED-	Negative	current driver	
32	LED-	Backlight	Negative feedback constant	
32	LED-	Negative	current driver	
33	NC			
34	NC	S		
35	NC			
36	NC			
37	NC			
38	NC			
20	IED:	Backlight	Packlight Dower Docking	
39	LED+	Positive	Backlight Power Positive	
40	LED+	Backlight	Backlight Power Positive	

Positive
----------

- 1. Default sticker 40 PIN 0.5mm Flip-Top
- 2. Please note that there is a 1-pin start bit on the main board.

### MIPI2 (40PIN 0.5MM Flip-Top)

Serial numb er	Definition	Attribute	Description	40.,
1	VDD1V8	1.8V	VDD1.8V power supply	
2	VDD3V3	3.3V	VDD3.3V power supply	
3	VDD3V3	3.3V	VDD3.3V power supply	
4	NC			
5	RESET	Screen Reset	Screen reset, low effective	
6	NC			00
7	GND	Ground	Ground	000
8	MIPI-DO-	Data channel 0	Data bit	
N		Negative		The red box marks the
		Data		first leg
9	MIPI-DO+	channel 0 Positive	Data bit	
10	GND	Ground	Ground	

11	MIPI-D1-	Data channel 1 Negative	Data bit	
12	MIPI-D1+	Data channel 1 Positive	Data bit	60.
13	GND	Ground	Ground	C
14	MIPI-CLK-	Clock Negative	Clock position	
15	MIPI-CLK+	Clock Positive	Clock position	
16	GND	Ground	Ground	
17	MIPI-D2-	Data channel 2 Negative	Data bit	
18	MIPI-D2+	Data channel 2 Positive	Data bit	
19	GND	Ground	Ground	
20	MIPI-D3-	Data channel 3 Negative	Data bit	

		Data	
21	MIPI-D3+	channel 3	Data bit
		Positive	
22	GND	Ground	Ground
23	NC		
24	NC		$C_{O}$ .
25	GND	Ground	Ground
26	NC		
27	NC		
28	NC		
29	NC		
30	GND	Ground	Ground
21	IED	Backlight	Negative feedback constant
31	LED-	Negative	current driver
22	LED	Backlight	Negative feedback constant
32	LED-	Negative	current driver
33	NC		
34	NC		
35	NC		
36	NC		
37	NC		
38	NC		

39	LED+	Backlight	Racklight Dower Positive	
39	LED+	Positive	Backlight Power Positive	
40	LED+	Backlight	Packlight Dower Desitive	
40	LED+	Positive	Backlight Power Positive	

- 1. Default stick 40 PIN 0.5mm flip type;
- 2. There is 1 pin start bit on the main board.

### PCI-E Socket

Serial numbe	Definition	Attribute	Description	
1	MIC+	Microphon e +	4G microphone input positive	
2	3G-VCC	power	3.7V Input	
3	MIC-	Microphon e -	G microphone input negative	
4	GND	GND	Ground	
5	SPKR+	Right channel +	4G speaker output positive	
7	SPKR-	Right channel -	4G speaker output negative	
8	SIM_VCC	SIM card		

		power		VI
		supply		
9	GND	GND	Ground	
10	SIM_IO	SIM card		
		data		2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
12	CIM CIV	SIM card		
12	SIM_CLK	clock		
14	SIM_RESET	SIM card		20
14	SIIVI_IXESET	reset		
15	GND	GND	Ground	
16	SIM_VPP	SIM_VPP	Floatable	
18	GND	GND	Ground	
20	4G-ON/OF	Modular	Till dayn gwiteb	
20	F	Switcher	Pull-down switch	
21	GND	GND	Ground	
	(5)	Module		
22	3G-RESET	reset		
24	3G-VCC	PCIE power	3.7V Input	
		supply		
26	GND	GND	Ground	
27	GND	GND	Ground	
29	GND	GND	Ground	

34	GND	GND	Ground	
35	GND	GND	Ground	
36	4G-DM	USB-DM	USB Data Negative	
37	GND	GND	Ground	
38	4G-DP	USB-DP	USB Data Positive	
39	3G-VCC	PCIE power	3.7V Input	$C_{O}$ .
		supply		2
40	GND	GND	Ground	5
41	3G-VCC	PCIE power	3.7V Input	
		supply	.c.Cii.i	
43	GND	GND	Ground	
50	GND	GND	Ground	
52	3G-VCC	PCIE power	PCIE power supply	
<i>JL</i>	30 700	supply	Tell power supply	
6, 11,		(5)		
13, 17,	(%			
19, 23,				
25, 28,	NC	NC		
30, 31,	NC	NC		
32, 33,				
42, 44,				
45, 46,				

47, 48,		
49, 51		

## HDMI\_IN (FPC electric socket 0.5MM)

Serial				
numb	Definition	Attribute	Description	
er				00.
1	I2C4-SCL	Output	SCL signal	2
2	I2C4-SDA	Output	SDA signal	
3	I2S3-SDI	Output	I2S in-group signals	
4	I2S3-LRCK	Output	I2S in-group signals	SV 🗎
5	I2S-SCLK	Output	I2S in-group signals	
6	I2S-MCLK	Output	I2S in-group signals	15 GE
7	RST	Ground	Poset signal	
/	אסו	Wire	Reset signal	
8	LIDMLID	Ground	Ro ponding	
0	HDMI-IR	Wire	Be pending	红色方框标记为第一脚
9	STBY	Output	Standby control	The red box marks the
10	INT	Output	Interrupt signal	first leg
11	CLKP	Output	mipi clock channel positive	
12	CLKN	Output	mipi clock channel negative	
13	D3P	Output	mipi data channel 3 positive	
14	D3N	Output	mipi data channel 3 negative	

15	D2P	Output	mipi data channel 2 positive	
16	D2N	Output	mipi data channel 2 negative	
17	D1P	Output	mipi data channel 1 positive	
18	D1N	Output	mipi data channel 1 negative	
19 [	DOP	Ground	mipi data channel 0 positive	~ co.,
		Wire		
20 D0	DON	Ground	mipi data channel 0 negative	
		Wire		5
21	GND	Output	Ground Wire	
22	GND	Output	Ground Wire	
23	PWREN	Output	Power Enable	
24	VCC	Output	5V Output	

1. There is a 1-pin start position on the motherboard, please pay attention

The following is the built-in socket interface definition

DC socket → Standard 12V round head 6.0MM aperture, 2.0MM inner pin, inner positive and outer negative

TF card → Standard TF card socket interface definition

HDMI → Standard A-type HDNI socket definition

RJ45 → Standard 1000M RJ45 socket definition

USB-OTG  $\rightarrow$  Standard USB3.0 large horizontal socket definition (OTG or HOST function can be set in the system)

USB-HOST → Standard USB3.0 large horizontal socket definition

SIM card → Standard SIM card interface definition

#### Note:

- 1. The total current of the 7 USB ports should not exceed 2.0A;
- 2. The total current of 3.3V should not exceed 1A;
- 3. The INT and GND at the bottom of the motherboard are short-circuited for the remote control learning function. If you need it, can contact our sales staff to get the DXF structure diagram.

# **Working Parameters**

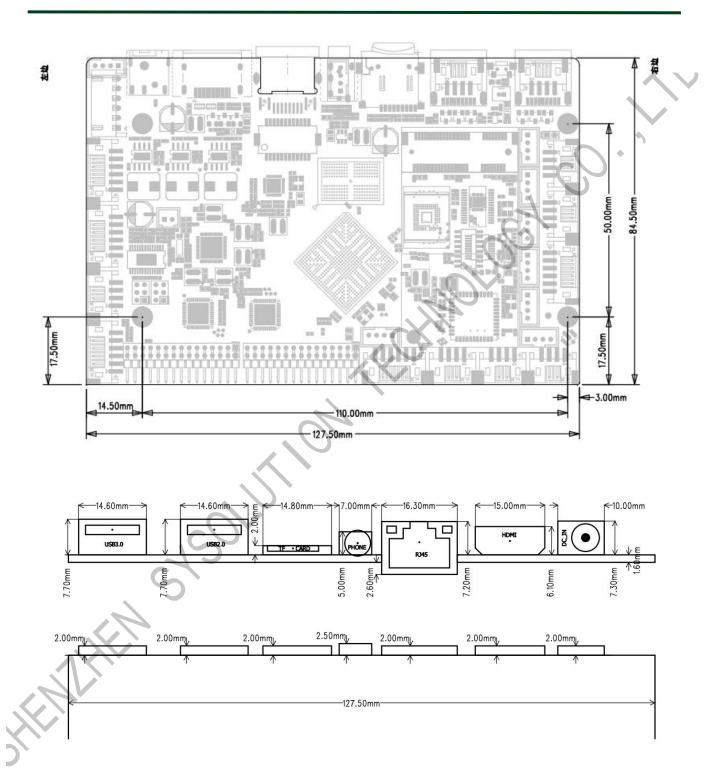
Electronic device materials				
PCB board	FR4 6-layer board, immersion gold impedance board, TG150, matte black			
Electronic	Lead-free, halogen-free and environmentally friendly materials (original and			
materials	authentic, in line with ROSH requirements)			
Production	Lead-free, environmentally friendly production process (in compliance with			
Process	ISO9001 production quality management system requirements)			
Electrical parame	Electrical parameters (bare card)			
Parameter	Minimum	Standard value	Maximum	Unit
Working	9	12V	14	V
Voltage  Working  current	104	210	387	mA
Shutdown	4.88	4.93	5	mA
Motherboard power	1.2	2.5	4.6	W
Speaker output power (8R speaker)	4	4.5	5	w

RTC operating	0.477	0.512	0.553	uA
current	0.477	0.512	0.555	uA
USB output				
current	1810	2020	2340	mA
( 5V )*1				
UART output				$\mathcal{O}_{\mathcal{O}}$ .
current	930	1200	1310	mA
(3.3V) *2			,00	
Working	-10	Normal	70	°C
temperature	-10	temperature	70	
Storage	-20	Normal	80	°C
temperature	-20	temperature	00	C

#### Note:

- 1. \*1\*2 is the sum of the output power of the same voltage of the motherboard. The specific output power of each interface socket shall be subject to the interface description.
- 2. Considering the overall working conditions, the whole machine works in an environment outside the limit value, and the working performance of the whole machine cannot be fully guaranteed.
- The working current value is the latest firmware. Due to the subsequent firmware update, the working current size will be slightly different, which is within the normal range. The specific firmware before the product is shipped shall prevail.

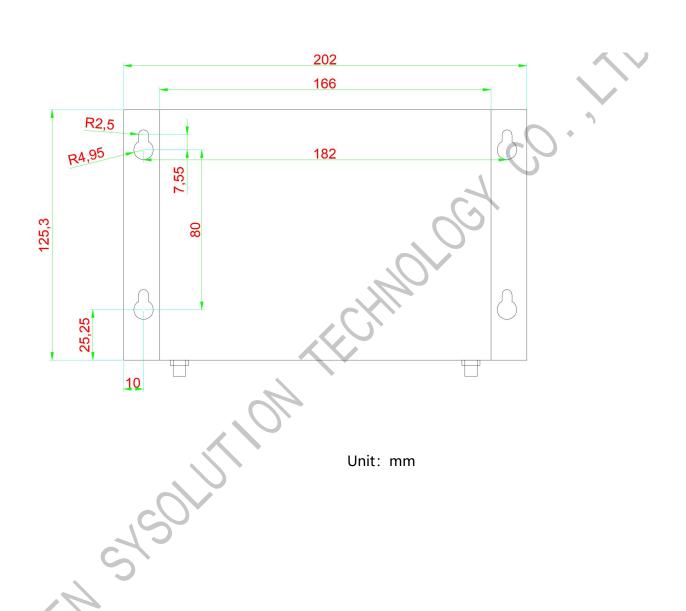
## **Bare card installation dimensions**



PCBA size: 127.5mm\*84.50mm/±0.5mm; board thickness 1.6mm±10%

Screw hole specifications: ∮3.0mm x 4/±10%

## Size diagram with box



## **Cautions**

- During the assembly process, please be careful not to operate the connection wiring with power on;
- When touching the PCBA motherboard, must wear anti-static wristbands (sleeves) and other anti-static protection tools;
- When connecting external devices to the motherboard, the PIN definition must be strictly checked to avoid wrong or reverse connection;
- 4. During the installation and fixing process, it is strictly forbidden to cause board deformation and other problems due to various reasons;
- 5. During the installation process, it is strictly forbidden to stack multiple boards together or short-circuit with other peripherals;
- 6. During the installation process, do not tie sensitive signal lines together with power lines, such as WIFI antennas/data cables;
- 7. When installing the LCD screen, be sure to pay attention to the selection of screen voltage, the size of the current, and the position of the first pin;
- 8. When installing the LCD screen, be sure to pay attention to the backlight voltage and whether the current is within the power range of the power adapter;
- 9. When connecting peripheral devices, pay attention to the level matching of the peripheral data and whether the current size meets the requirements;
- 10. When installing the serial port, pay attention to the type of serial port device being connected and whether the TX and RX pins are connected in reverse;

11. Consider the overall power. After the entire device is connected, what is the overall power and whether the power supply is sufficient.

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